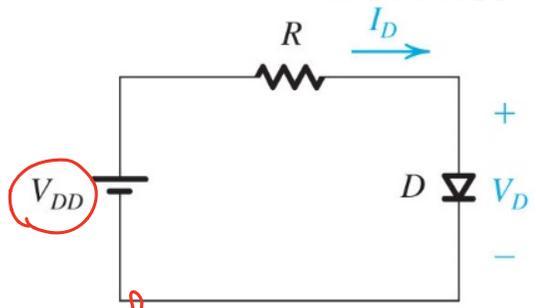
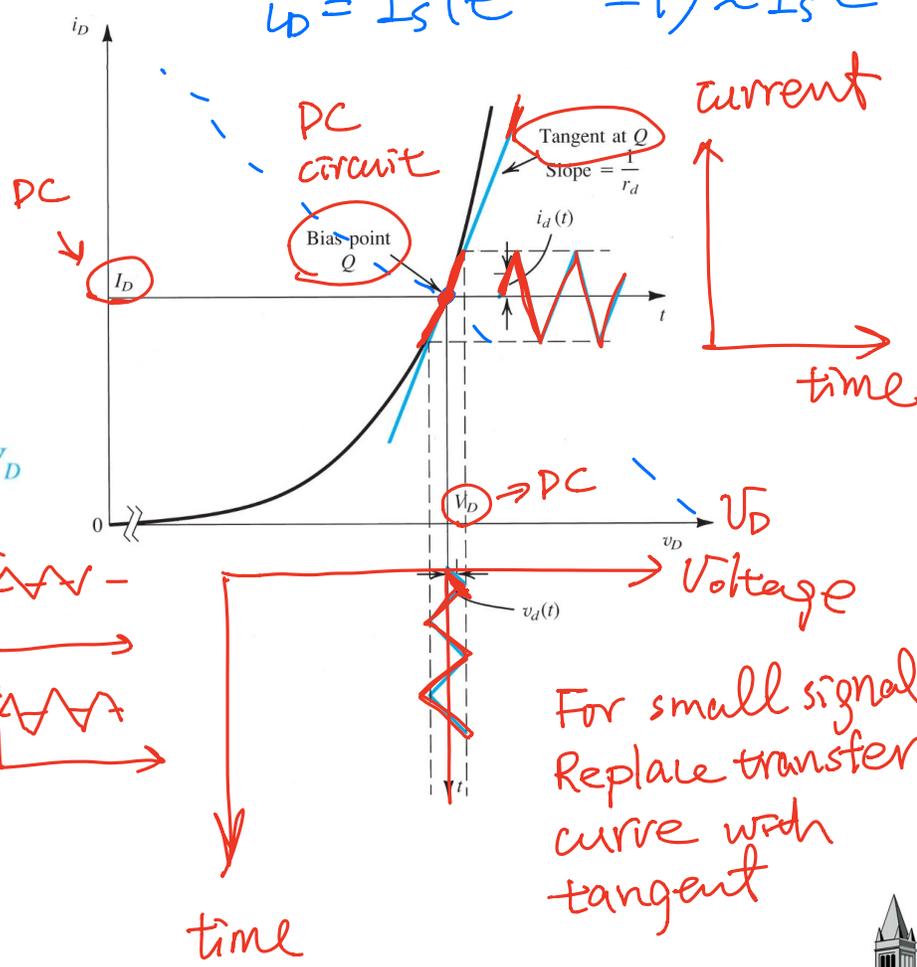
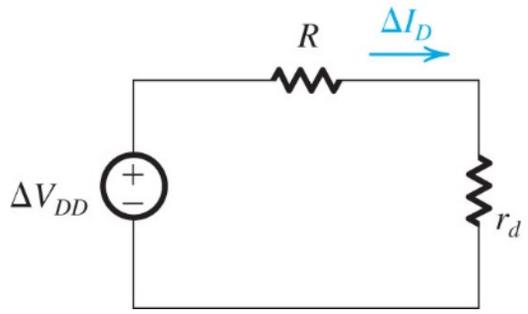
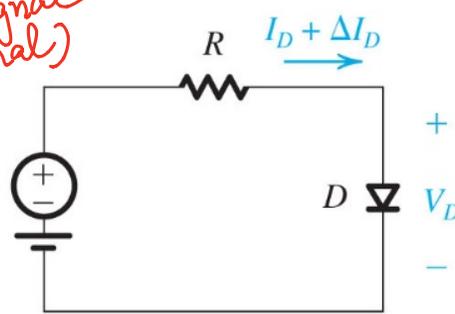


Small Signal Resistance

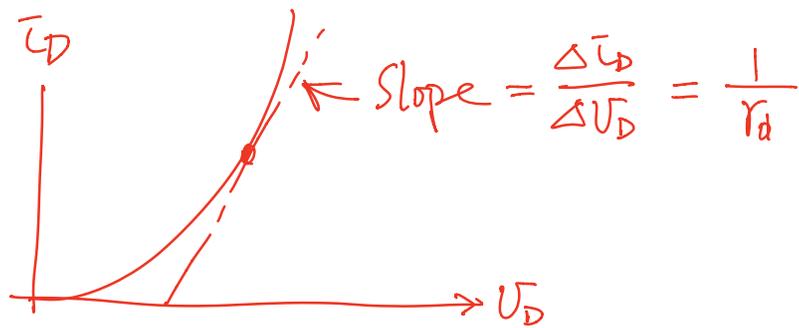
$$i_D = I_S (e^{v_D/V_T} - 1) \approx I_S e^{v_D/V_T}$$



small signal
(ac signal)



For small signal
Replace transfer
curve with
tangent



$$r_d = \frac{\Delta U_D}{\Delta \bar{i}_D} = \frac{1}{\left(\frac{d\bar{i}_D}{dU_D}\right)} = \frac{1}{\left(\frac{I_D}{V_T}\right)} = \frac{V_T}{I_D}$$

$V_T = 26 \text{ mV}$
at room
temp

$$\bar{i}_D = I_S e^{\frac{U_D}{V_T}}$$

$$\frac{d\bar{i}_D}{dU_D} = I_S \cdot \frac{1}{V_T} e^{\frac{U_D}{V_T}} = \frac{1}{V_T} (I_S e^{\frac{U_D}{V_T}}) = \frac{I_D}{V_T}$$

Symbol convention

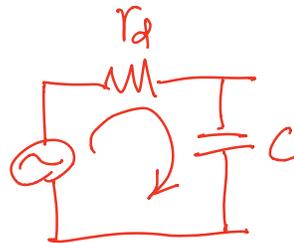
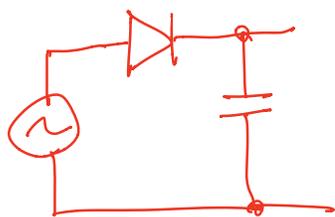
I_D = DC current

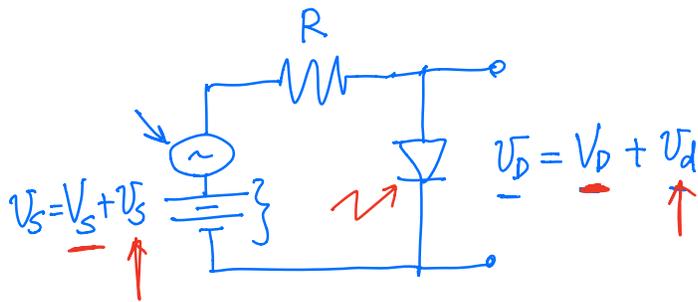
\bar{i}_D = AC current

If diode is biased at $I_D = 1 \text{ mA}$

$$r_d = \frac{V_T}{I_D} = 26 \Omega$$

$$RC = r_d \cdot C$$





Previously solved
 by iteration
 → DC solution

$$\begin{cases} I_D = I_s e^{V_D/V_T} \\ V_s = I_D R + V_D \end{cases}$$

$v_D = V_D + v_d$
 ↑ ↑ ← ac small signal, 
 ↑ ← DC: bias $|v_d| \ll |V_D|$
 Total signal
 [small-case symbols
 Capital subscript

$$\bar{i}_D = I_s e^{\frac{V_D}{V_T}}$$

$$\begin{aligned} \bar{i}_D &= I_D + \bar{i}_d \\ v_D &= V_D + v_d \end{aligned}$$

$$\begin{aligned} \bar{i}_D + \bar{i}_d &= I_s e^{\frac{V_D + v_d}{V_T}} = f(V_D + v_d) = f(V_D) + \left. \frac{df}{dV} \right|_{V=V_D} \cdot v_d \\ &= I_s e^{\frac{V_D}{V_T}} + \underbrace{I_s \frac{1}{V_T} e^{\frac{V_D}{V_T}}}_{\frac{df}{dV}} \cdot \underbrace{v_d}_{\Delta V} \end{aligned}$$

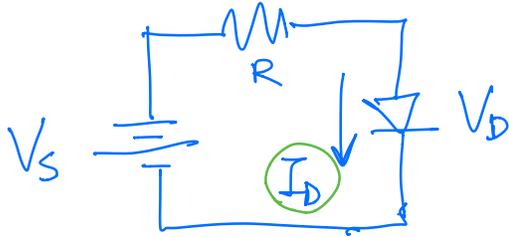
$$\bar{i}_d = \frac{I_D}{V_T} \cdot v_d$$

$$\frac{v_d}{\bar{i}_d} = \frac{V_T}{I_D} = r_d = \text{small-signal resistance}$$

2-step Process

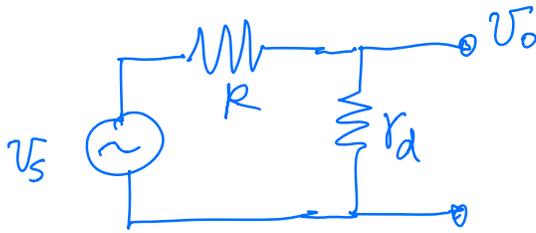
① Solve DC.

e.g. $V_D = 0.72 \text{ V}$
 $I_D = 1 \text{ mA}$



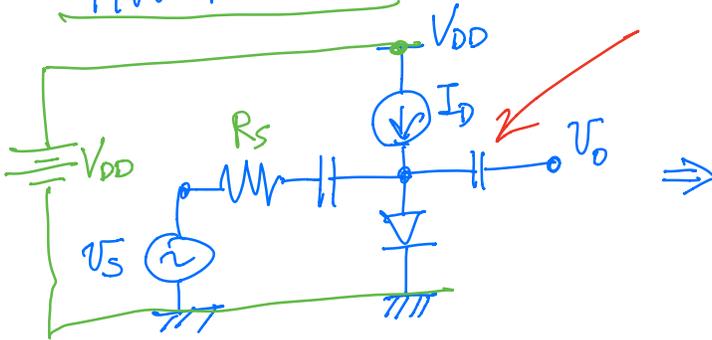
② Replace circuit with small-signal equivalent circuit

- * All DC voltage source \rightarrow short ckt
- * All DC current source \rightarrow open ckt
- * Replace diode with $r_d = \frac{V_T}{I_D}$

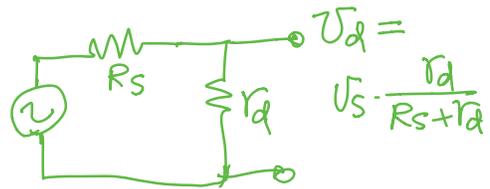


$$v_o = \frac{r_d}{R + r_d} v_s$$

HW Problem

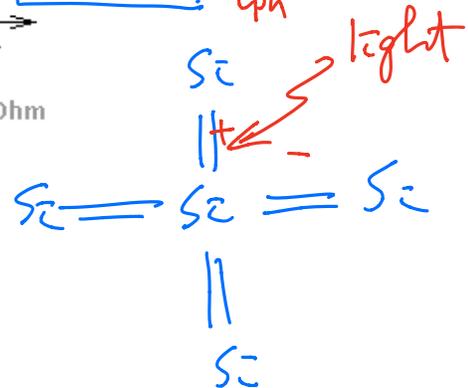
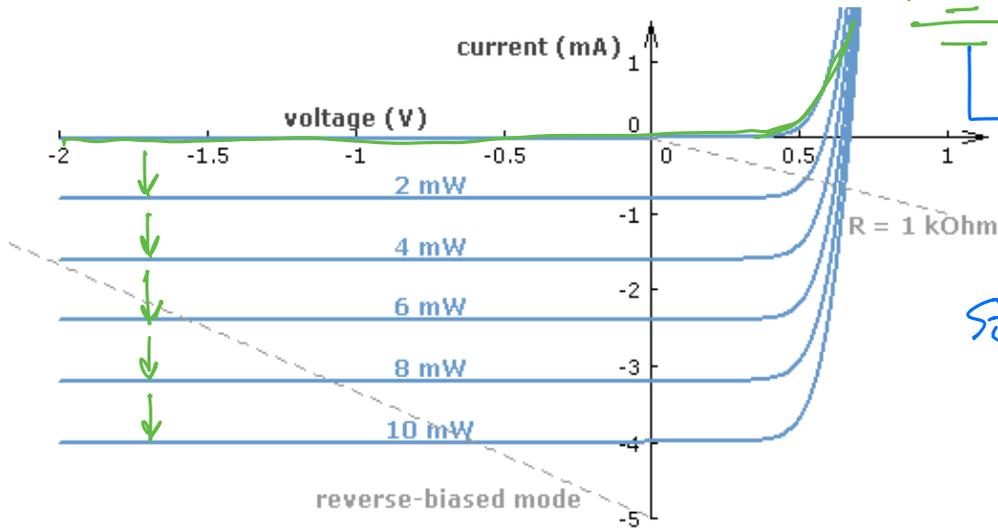
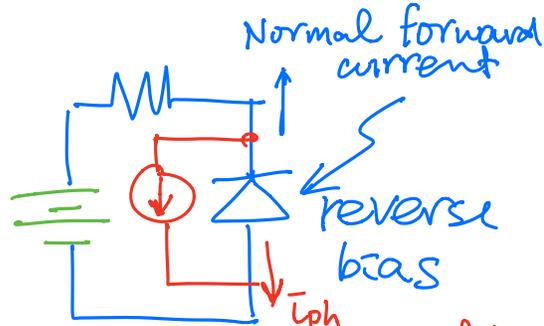


Coupling Capacitor, C large
 $Z_c = \frac{1}{j\omega C} \ll$ all resistance
 \rightarrow short ckt



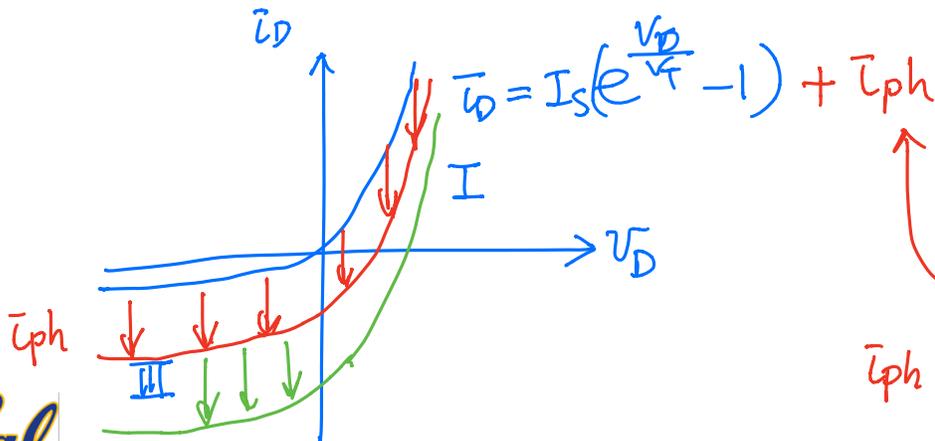
"3rd Quadrant" ←

Photodiodes

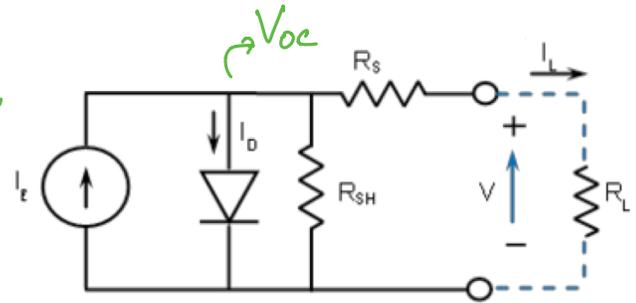
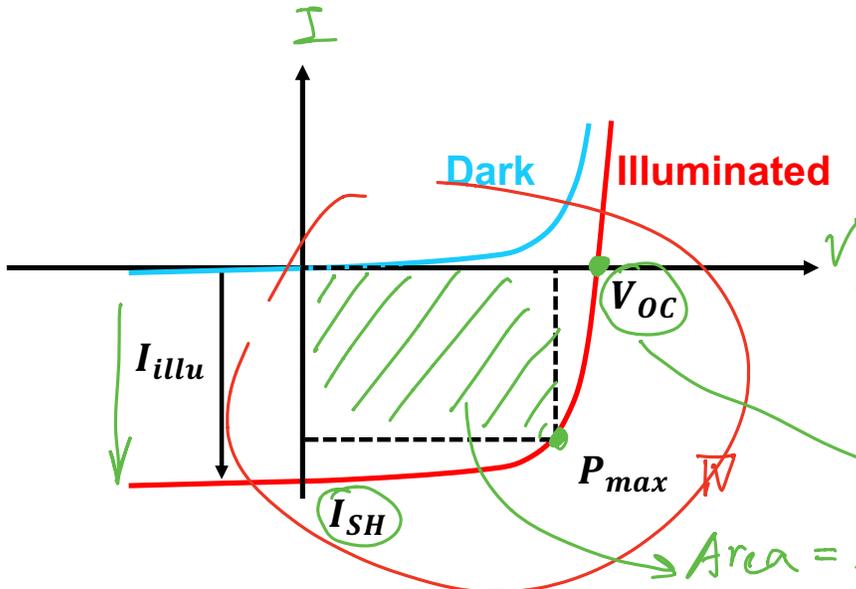


light create electron-hole pair.
→ Photocurrent

$I_{ph} \propto \text{light intensity}$



Solar (Photovoltaic, or PV) Cells



$I=0 \Rightarrow V_{oc} = \text{open-circuit voltage}$
 Area = $I \cdot V = \text{generated energy}$

- Operating in the 4th quadrant of the I-V curve energy
 → It generates power !

- Key parameters:
 - Open circuit voltage, V_{oc}
 - Short-circuit current, I_{sh}
 - Fill factor

PV ~ 20%
 ≤ 31% for Si PV

⇒ EE134